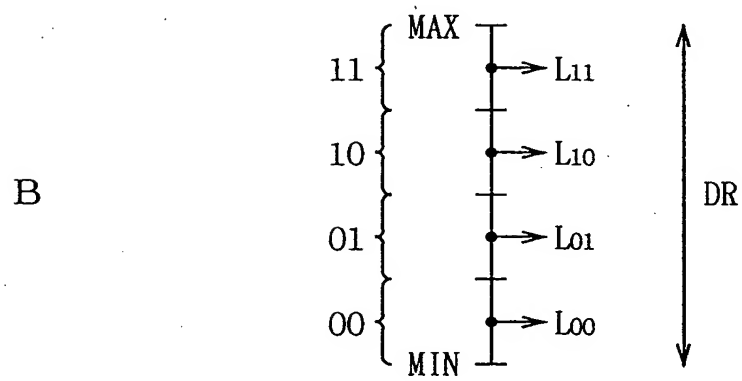
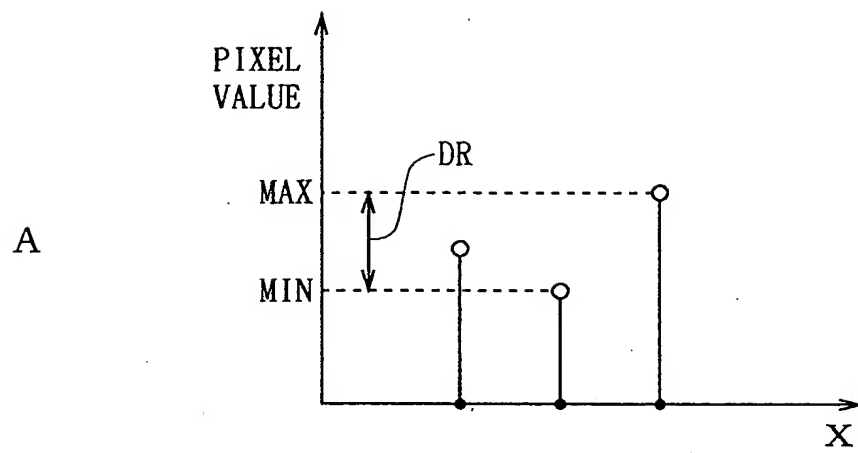


FIG. 1



ADRC PROCESSING

FIG. 2

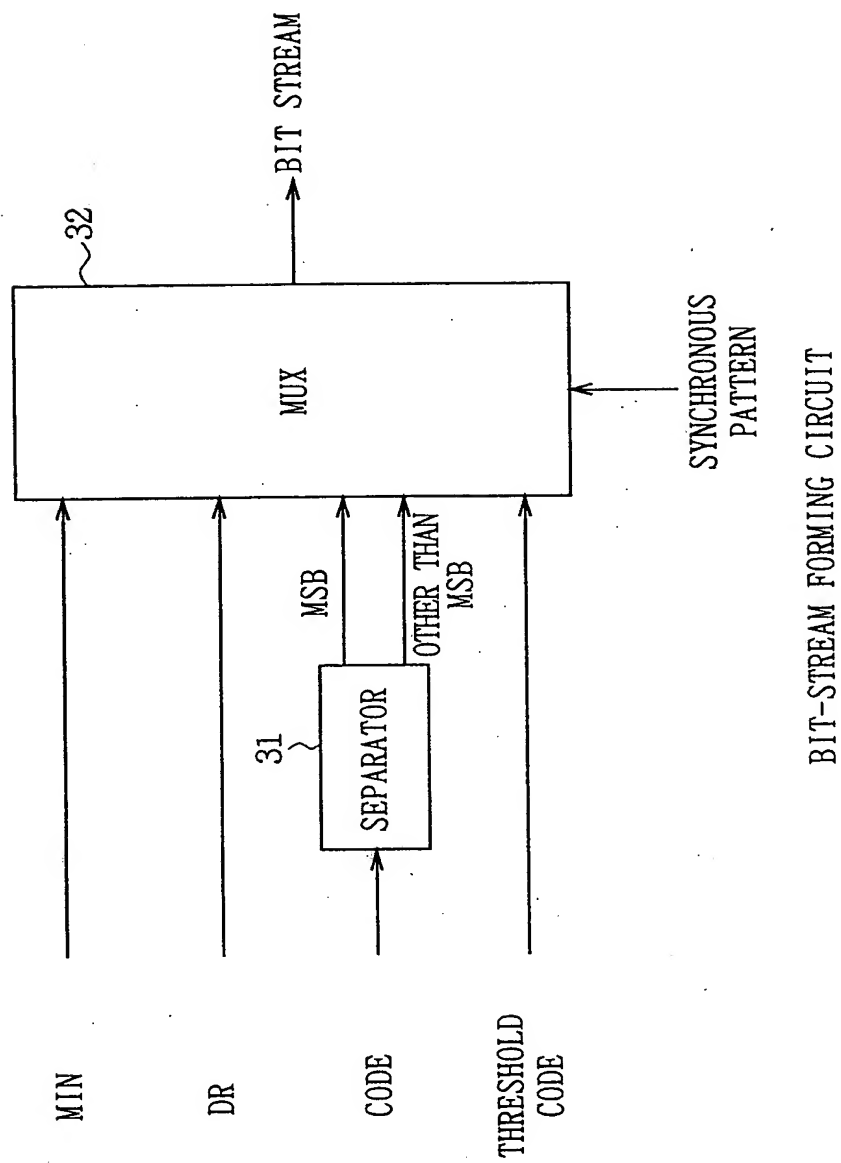


FIG. 4

BIT-STREAM FORMING CIRCUIT

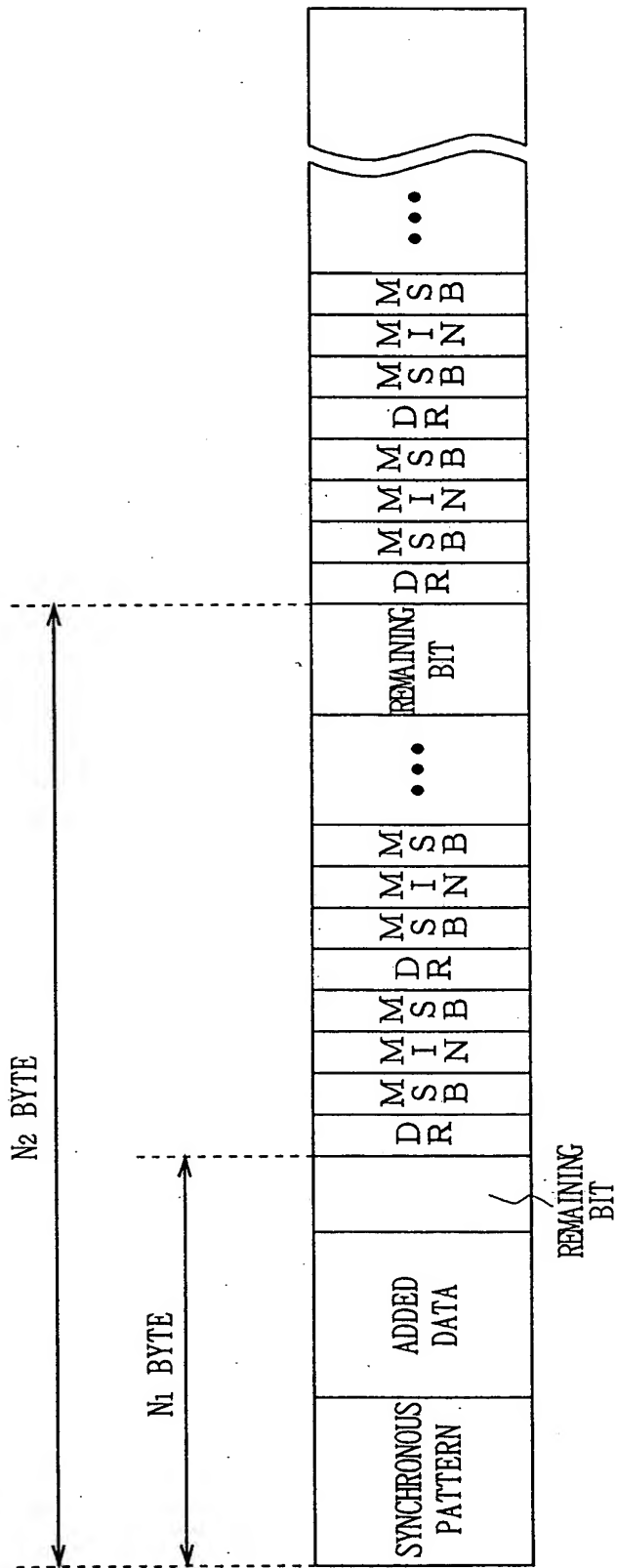


FIG. 5

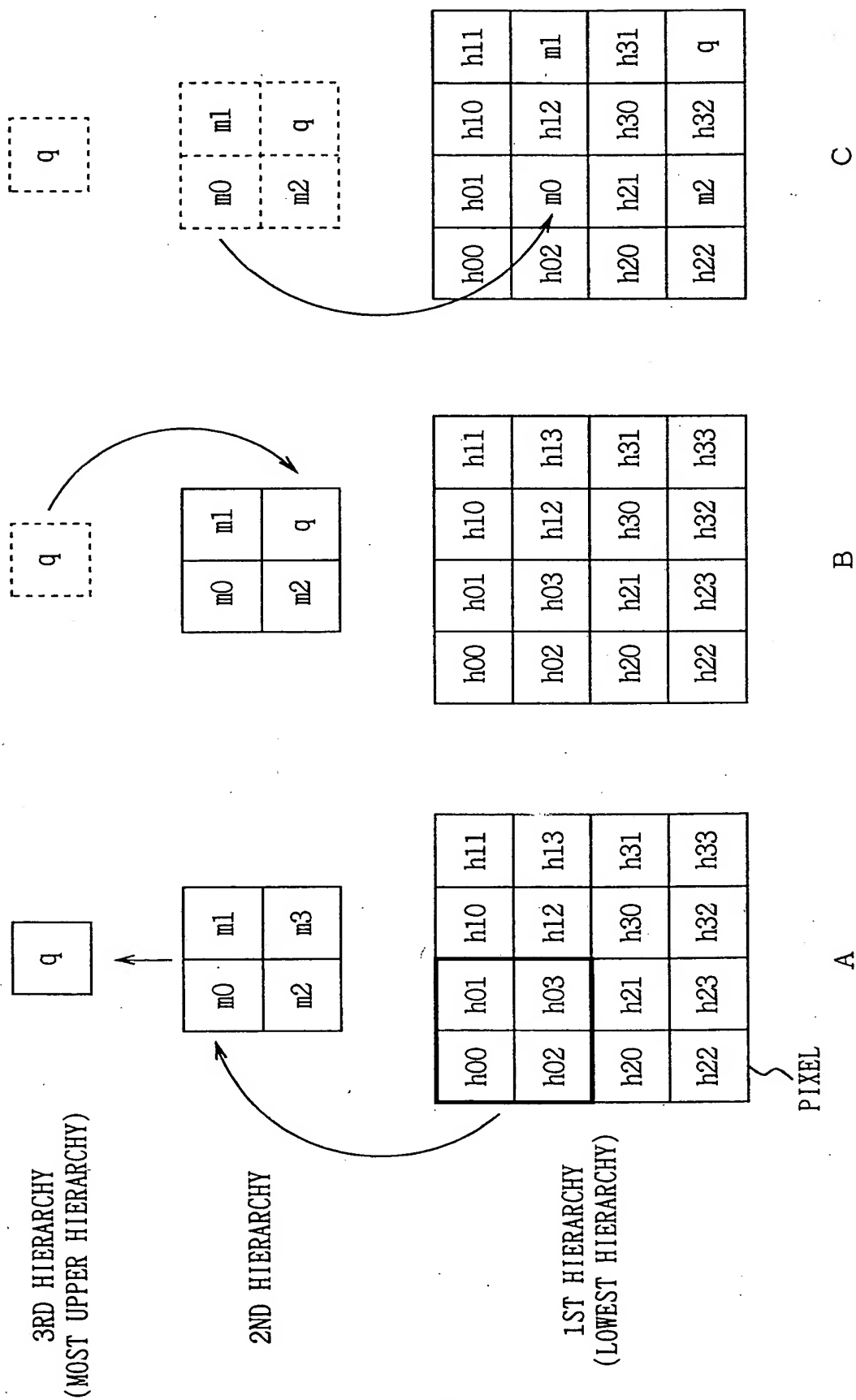
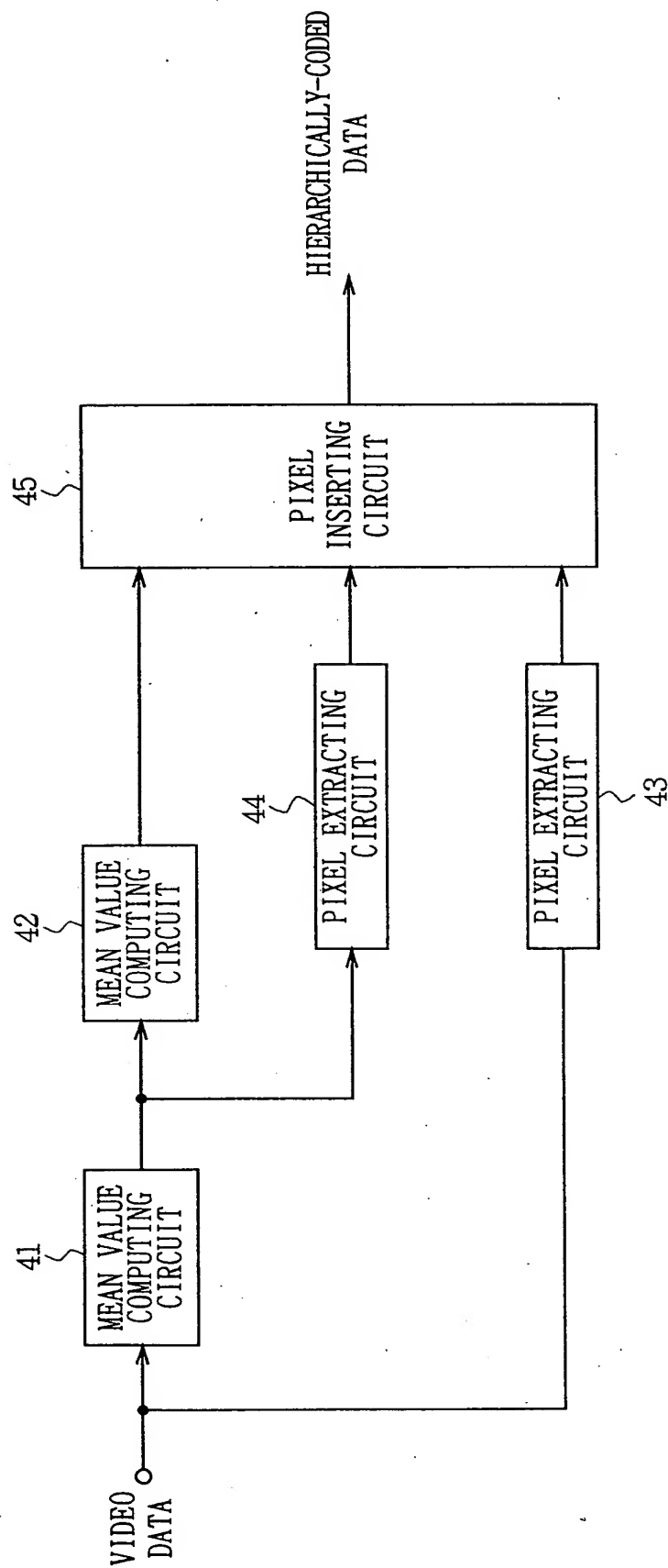


FIG. 6



HIERARCHICAL ENCODER

FIG. 7

ISDB TRANSMITTER.

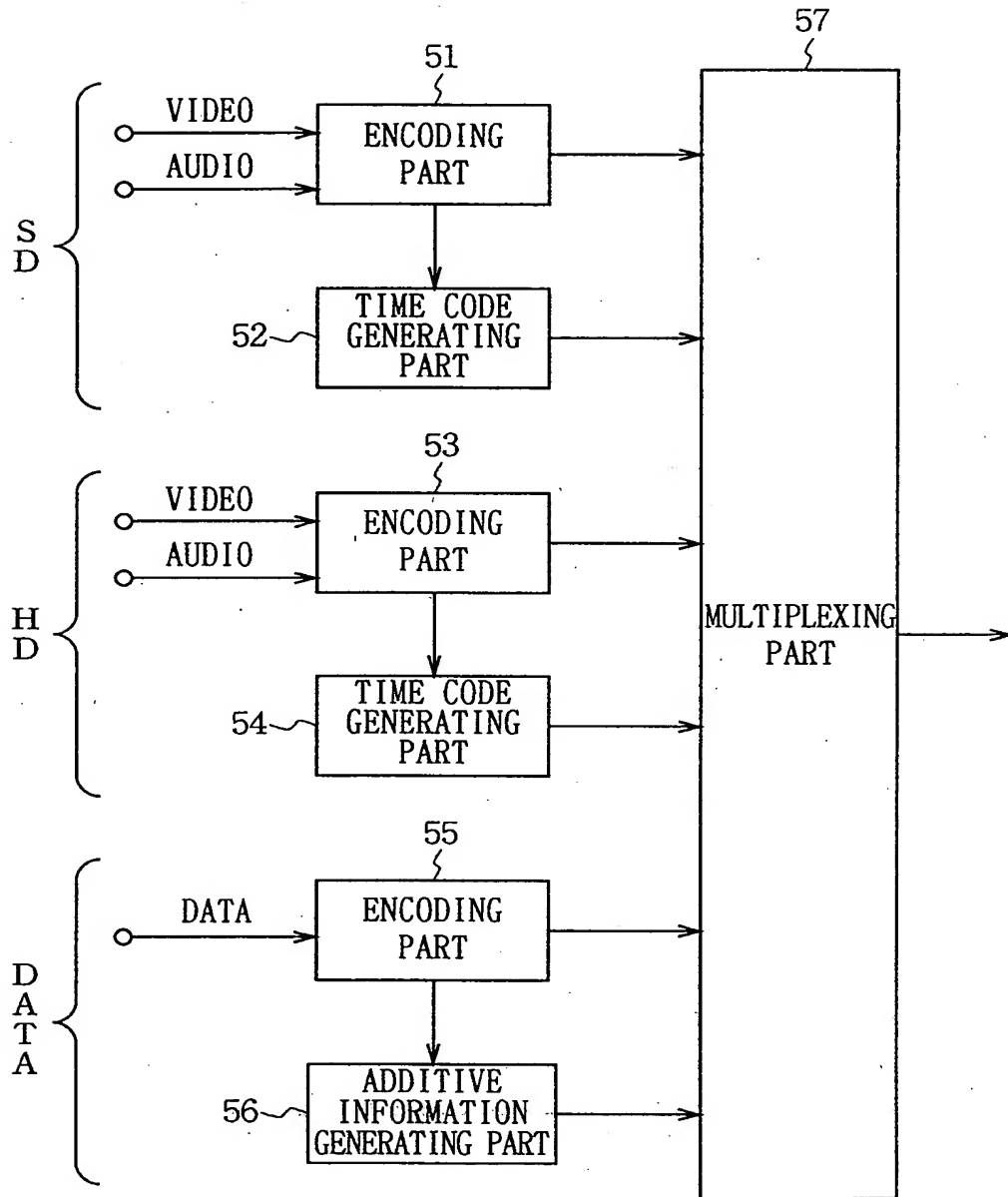
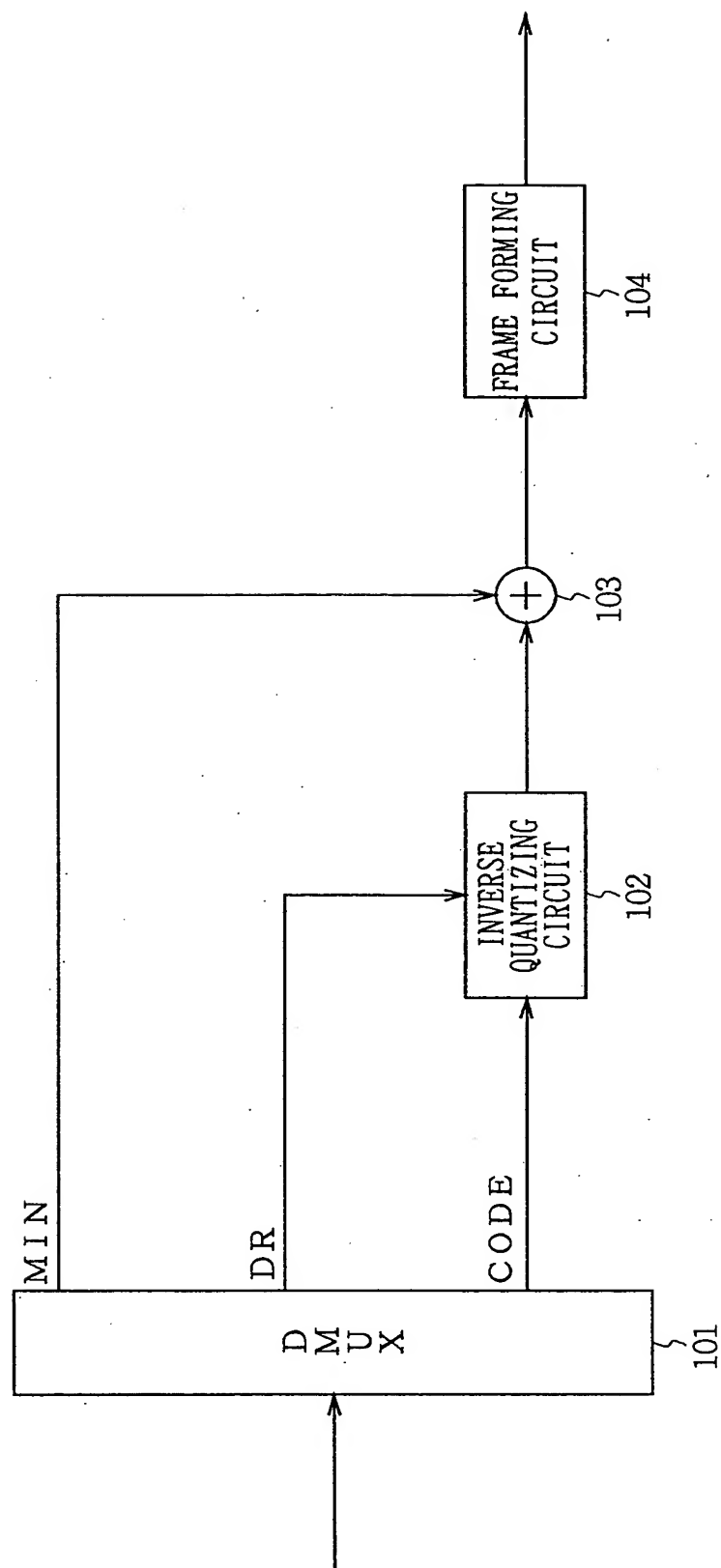


FIG. 8



ADRC DECODING CIRCUIT

FIG. 9

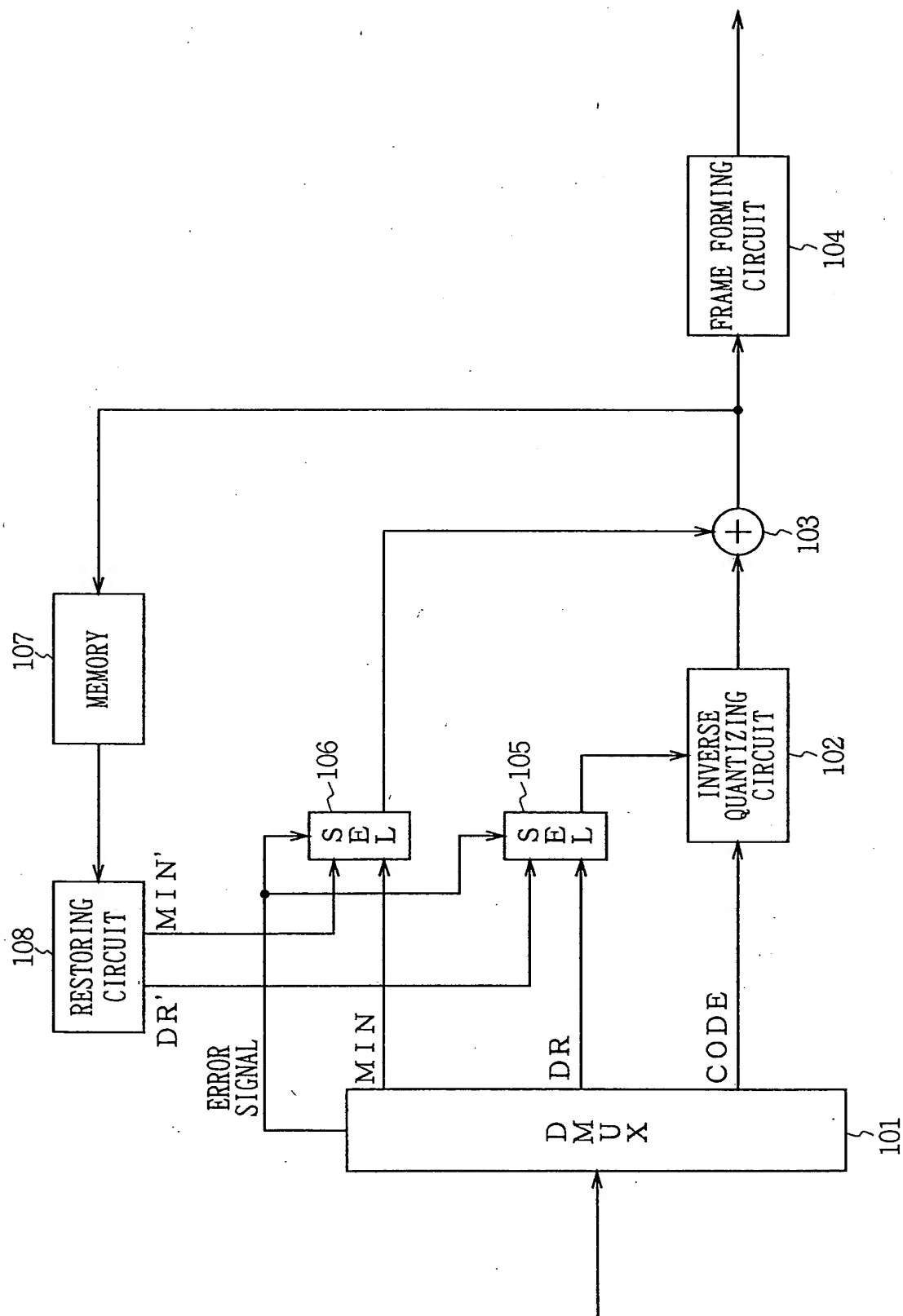
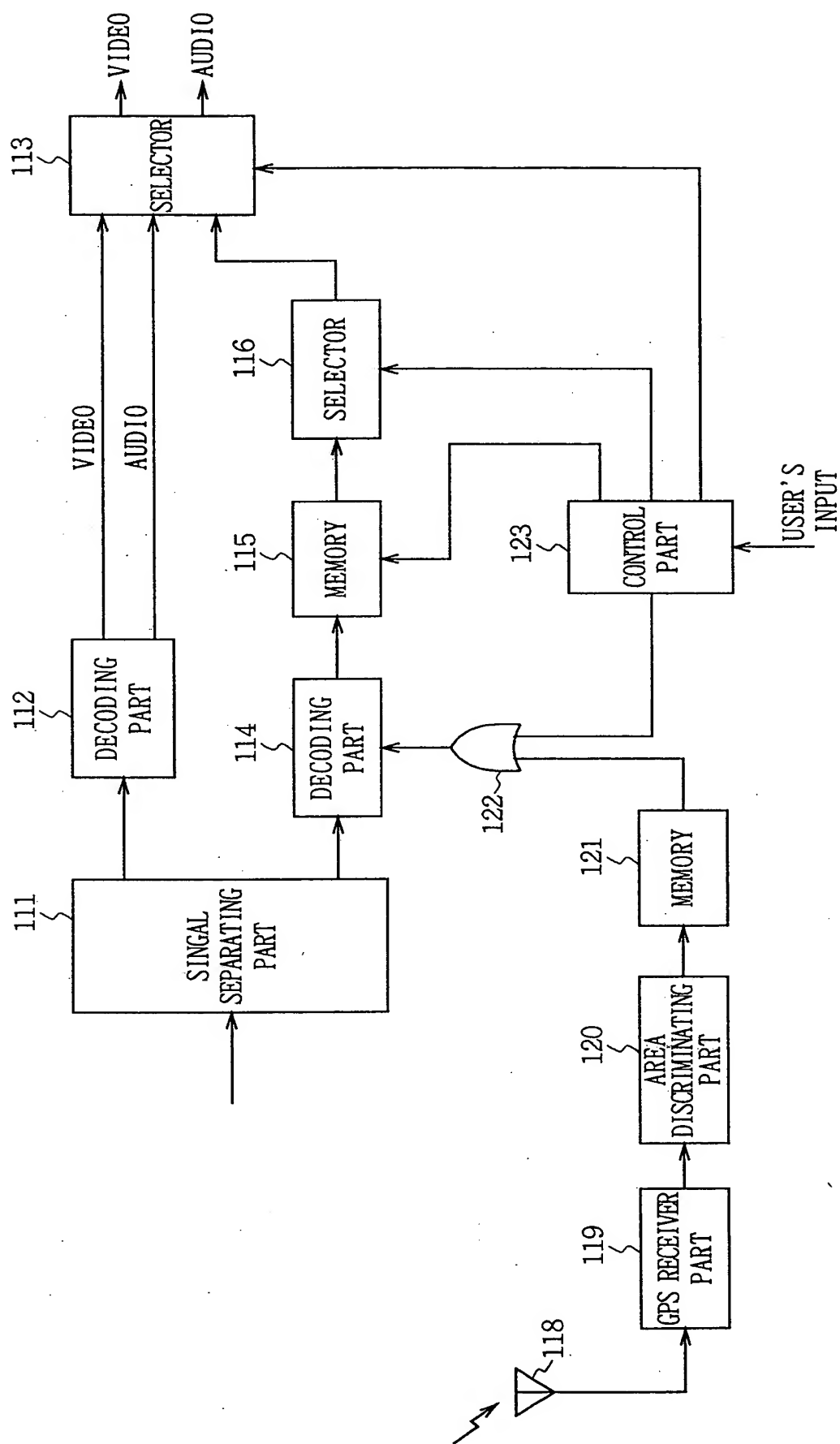
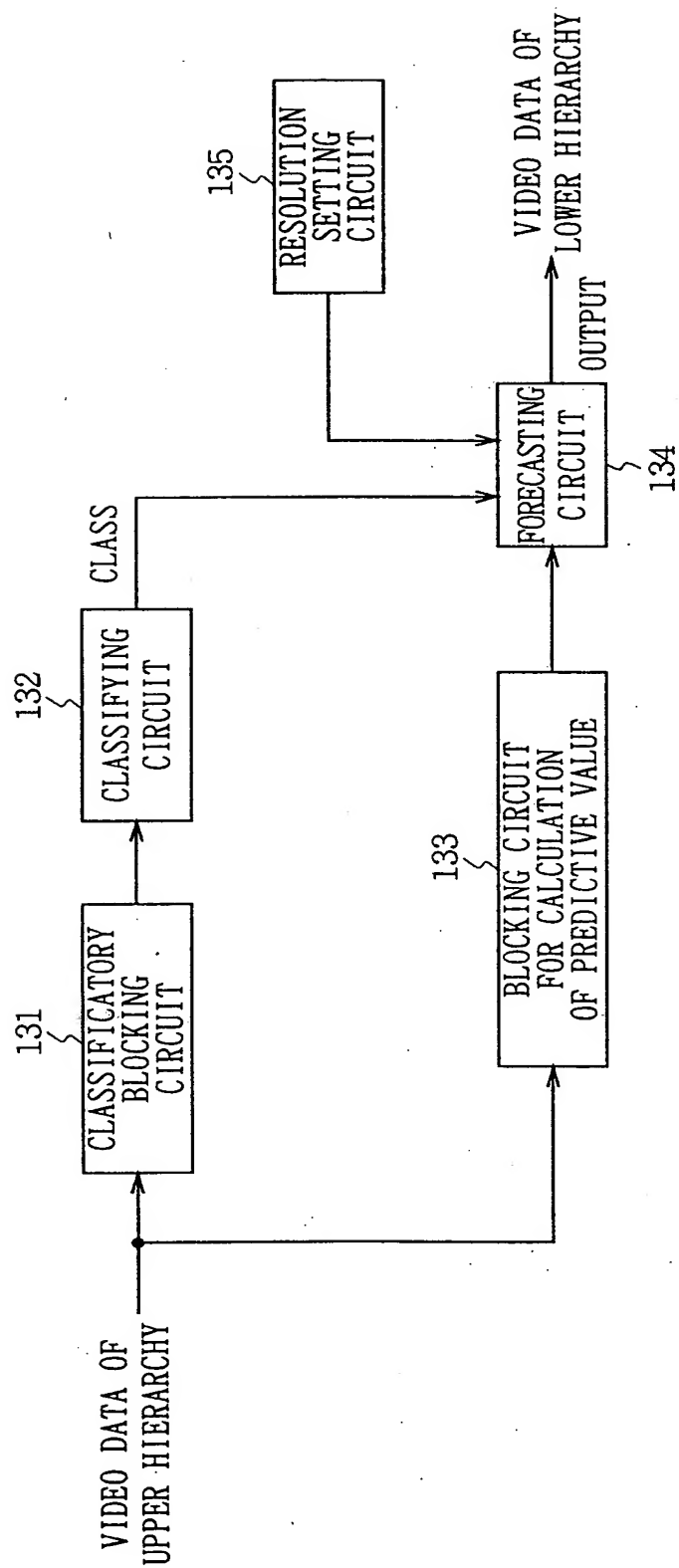


FIG. 10



ISDB RECEIVER

FIG. 11



RESOLUTION CREATING CIRCUIT

FIG. 12

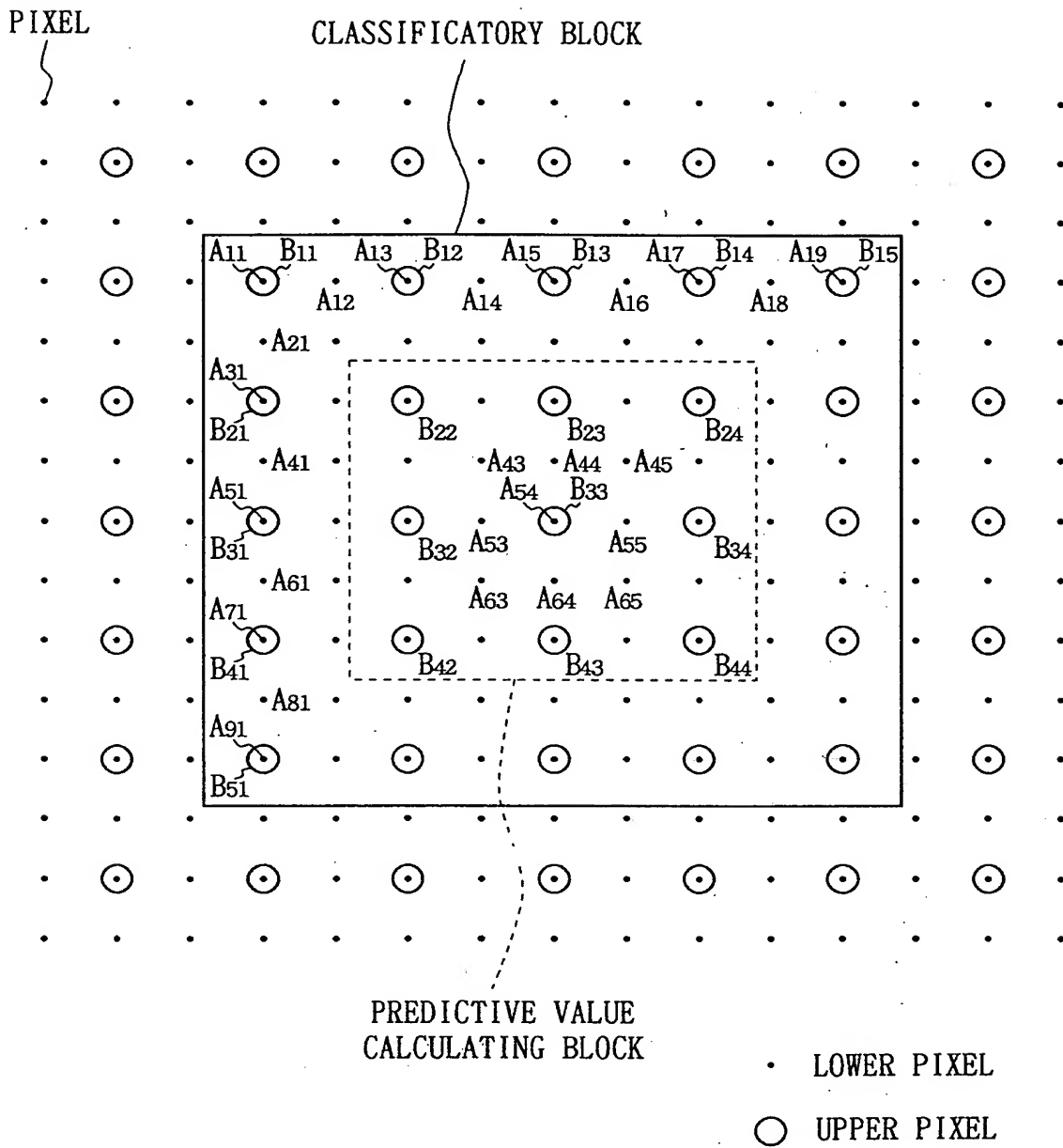


FIG. 13

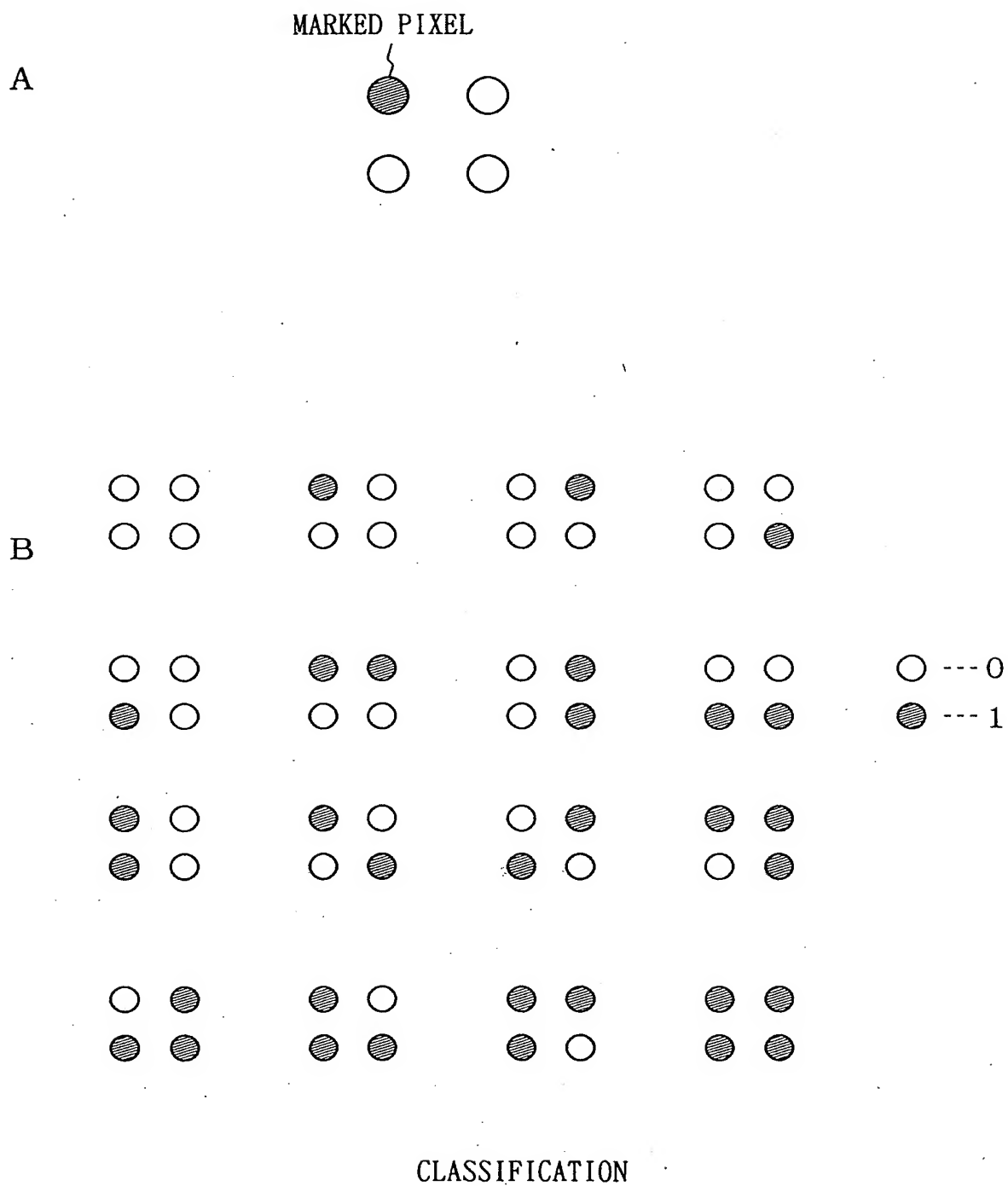


FIG. 14

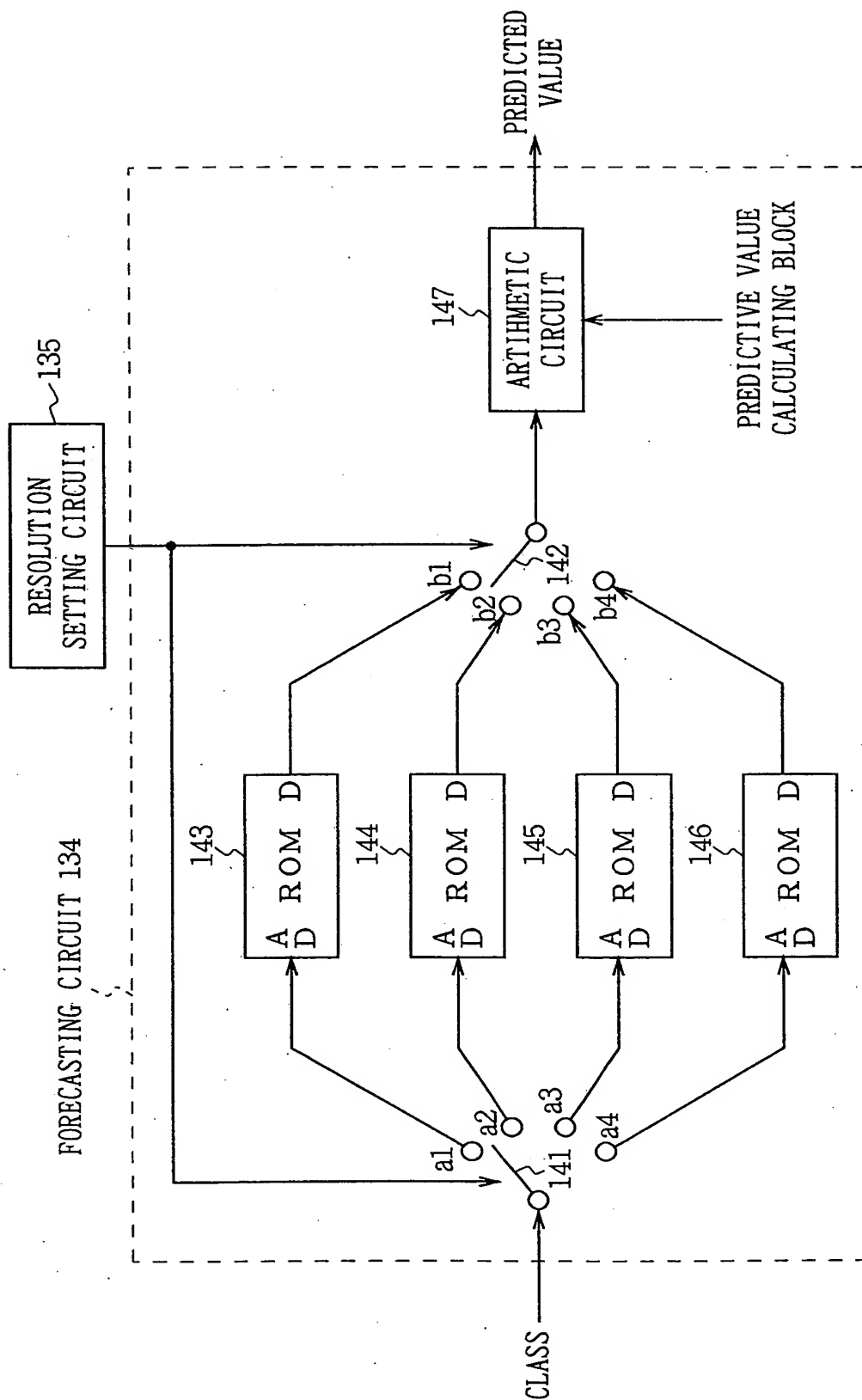


FIG. 15

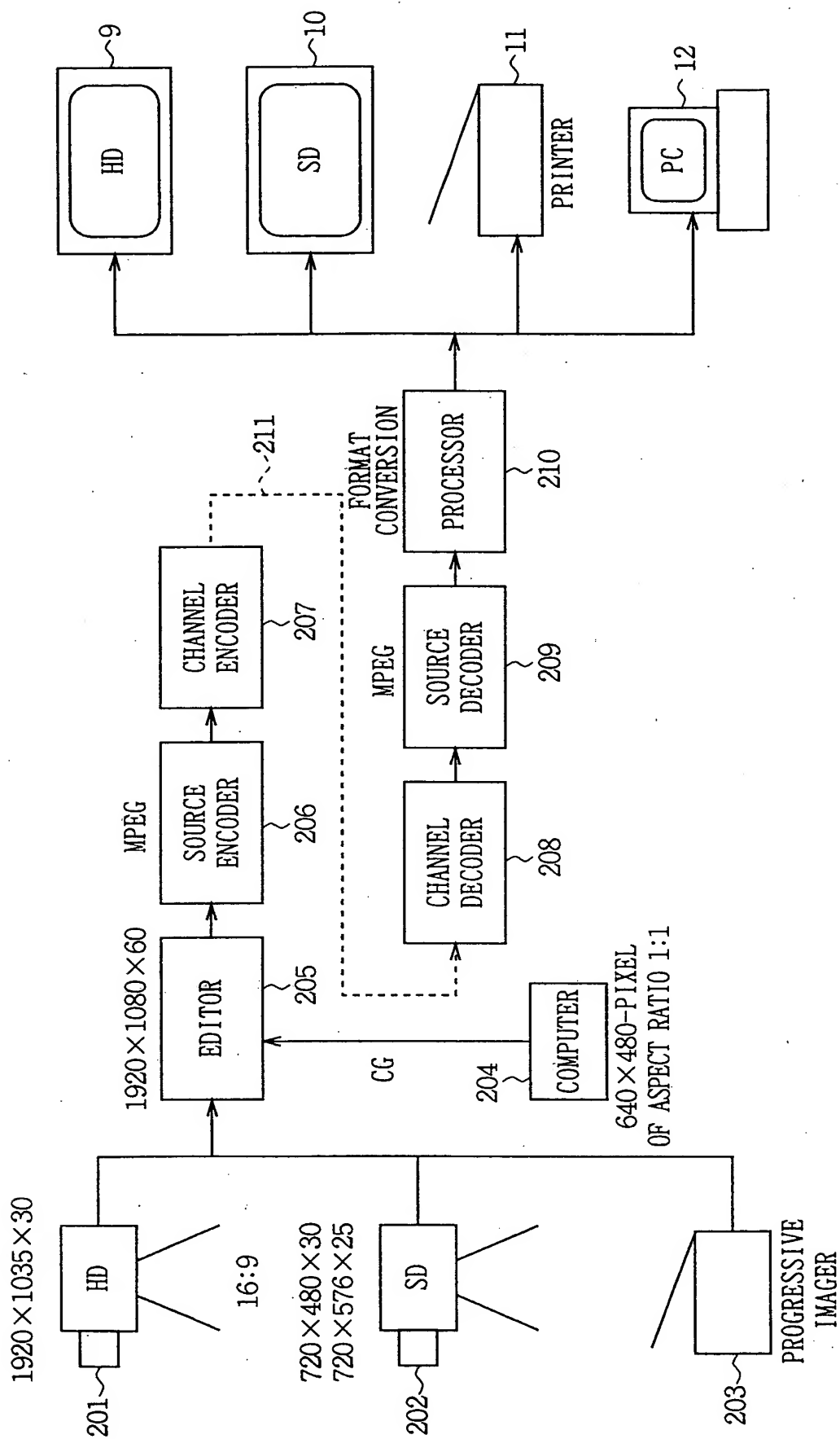


FIG. 16

Explanation of Reference Numerals

1, 2...video camera, 3...progressive imager, 4...computer, 5...network, 6...integrative encoding system, 7...adaptive decoding system, 8...converter for aspect ratio of pixel, 9, 10...display, 11...printer, 12...computer display, 13...transmission line, 21...blocking circuit, 22...minimum value detecting circuit, 23...maximum value detecting circuit, 24, 25...computing unit, 26...quantizing circuit, 31...separator, 32...multiplexer, 41, 42...mean value computing circuit, 43, 44...pixel extracting circuit, 45...pixel inserting circuit, 51...encoding part, 52...time code generating part, 53...encoding part, 54...time code generating part, 55...encoding part, 56...additive information generating part, 57...multiplexing part, 101...demultiplexer, 102...inverse quantizing circuit, 103...arithmetic unit, 104...frame forming circuit, 105, 106...selector, 107...memory, 108...restoring circuit, 111...signal separating part, 112...decoding part, 113...selector, 114...decoding part, 115...memory, 116...selector, 118...antenna, 119...GPS receiver part, 120...area discriminating part, 121...memory, 122...OR gate, 123...control part, 131...classificatory blocking circuit, 132...classifying circuit, 133...blocking circuit for calculation of predictive value, 134...forecasting circuit, 135...resolution setting circuit, 141, 142...switch, 143 - 146...ROM, 147...arithmetic circuit.